AN210323: Isomet Studio - Running Image mode example

Running the example Isomet iMS Studio Projects

Run the Isomet iMS Studio, e.g. from the desktop icon

1. Load Example Project

Is





In this case we select the file; D1384_100-140M_swp_100%A.iip

This IMAGE file generates a linear frequency ramp followed by an OFF period. It comprises of 120 image points with the same data on all four iMS4 channels. Points 1-100 are programmed with the 100-140MHz sweep at 100% amplitude. Points 101-119 are at an arbitrary frequency (131MHz) and 0% amplitude.

The **Sync Data (Dig)** field, is programmed with 0x0001 expect for 5 point around the mid-scan frequency (120 – 121 MHz). Use the L<-> R slider bar to display. The Sync Data outputs are inverted at the output J7. This data will give a logic high signal to indicate the mid-scan position

Project Explorer	• 4 ×	100	-140M swp× Enhance	K 📮 🚺 🕖 🕖 🤇 ed Tone	J () - () () () () () () () () () () () () ()						-	Signal Path 👻 💷 >
100-140M swp (120 entries)	<u>R</u>		Ch1 Frequency (MHz) Ch1 Amplitude (%)	Ch1 Phase (deg)	Ch2 Frequency (MHz)	Ch2 Amplitude (%)	Ch2 Phase (deg)	Ch3 Frequency (MHz)	Ch3 Amplitude (%)	Ch3 P ^	Power Settings
	+	•	100.0000	100.0000	0.0000	100.0000	100.0000	0.0000	100.0000	100.0000		
	—	1	100.4000	100.0000	0.0000	100.4000	100.0000	0.0000	100.4000	100.0000		
		2	100.8000	100.0000	0.0000	100.8000	100.0000	0.0000	100.8000	100.0000		
		3	101.2000	100.0000	0.0000	101.2000	100.0000	0.0000	101.2000	100.0000		
		4	101.6000	100.0000	0.0000	101.6000	100.0000	0.0000	101.6000	100.0000		
		5	102.0000	100.0000	0.0000	102.0000	100.0000	0.0000	102.0000	100.0000		
		6	102.4000	100.0000	0.0000	102.4000	100.0000	0.0000	102.4000	100.0000		Ciri Cirz Cirs Cir4
		7	102.8000	100.0000	0.0000	102.8000	100.0000	0.0000	102.8000	100.0000		Int Int Int Int
		8	103.2000	100.0000	0.0000	103.2000	100.0000	0.0000	103.2000	100.0000		DDS
		9	103.6000	100.0000	0.0000	103.6000	100.0000	0.0000	103.6000	100.0000		Amplifier Enable
		10	104.0000	100.0000	0.0000	104.0000	100.0000	0.0000	104.0000	100.0000		RF Channels 1+2 Enable
		11	104.4000	100.0000	0.0000	104.4000	100.0000	0.0000	104.4000	100.0000		—
		12	104.8000	100.0000	0.0000	104.8000	100.0000	0.0000	104.8000	100.0000		RF Channels 3+4 Enable
		13	105.2000	100.0000	0.0000	105.2000	100.0000	0.0000	105.2000	100.0000		Sync Data Settings
Compensation Functions		14	105.6000	100.0000	0.0000	105.6000	100.0000	0.0000	105.6000	100.0000		Analog Sync Output Source A
Name Entries	_	15	106.0000	100.0000	0.0000	106.0000	100.0000	0.0000	106.0000	100.0000		ImageAnalogA ~
	D	16	106.4000	100.0000	0.0000	106.4000	100.0000	0.0000	106.4000	100.0000		
		17	106.8000	100.0000	0.0000	106.8000	100.0000	0.0000	106.8000	100.0000	_	Analog Sync Output Source B
		18	107.2000	100.0000	0.0000	107.2000	100.0000	0.0000	107.2000	100.0000		Падеялаюдь
		19	107.6000	100.0000	0.0000	107.6000	100.0000	0.0000	107.6000	100.0000		Digital Sync Output Source
		20	108.0000	100.0000	0.0000	108.0000	100.0000	0.0000	108.0000	100.0000		ImageDigital Y
		21	108.4000	100.0000	0.0000	108.4000	100.0000	0.0000	108.4000	100.0000		
Tone Buffers		22	108.8000	100.0000	0.0000	108.8000	100.0000	0.0000	108.8000	100.0000	_	Digital Sync Output Delay
Name	_	23	109.2000	100.0000	0.0000	109.2000	100.0000	0.0000	109.2000	100.0000		
	D	24	109.6000	100.0000	0.0000	109.6000	100.0000	0.0000	109.6000	100.0000		0.00 🌩 µs
		25	110.0000	100.0000	0.0000	110.0000	100.0000	0.0000	110.0000	100.0000		Digital Sync Pulse Length
		26	110.4000	100.0000	0.0000	110.4000	100.0000	0.0000	110.4000	100.0000	~	Enabled
		<			·						>	0.01 🚔 🗤 из
		N	umber of Image Points:	120 🜩 Default Inte	ernal Clock Rate (kHz):	166.000 🗘 Default	External Clock Divider:	1				Sig Cal Pla Co
Hardware Console											/	
			Internal C	Clock rate				Tabs: S	ignal Path		Play	ver

The GUI window should look like this:

2. Select Compensation Tab

Click on Import Button Open the required *.LUT files

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e∓ N	ew fo	lder					•== -		?
ick access		^	Name	Date modified	Туре	Size			
eckton			20-220M_100%A_0ph.lut	05/05/2020 11:41	LUT File	57 KB			
ownloads	*		RvC 374nm100-140M-D90Ch50-A.LUT	25/01/2021 16:26	LUT File	225 KB			
ocuments	*								
File	File r	name:	RvC 374nm100-140M-D90Ch50-A.LUT	Isomet Compensation File (*.lut $ imes $					
						Open		Cancel	

LUT file size = 57KB is a *Global* compensation file that applies the same data to ALL outputs of the iMS4. (This is the only option for older Rev-A and Rev-B of the iMS4 Synthesizers).

LUT file size = 225KB is a Channel scoped (channel specific) compensation file that can apply unique values to each channel. This is the recommended LUT option for X-Y deflector use.

In this example we will open:

RvC 374nm100-140M-D90Ch50-A.LUT

[This Compensation file generated for a D1384 at 374nm. 100-140MHz freq' range using a RFA0120-4-15 amp. iMS4 Power Settings DDS=90% , Chn=50% (See Signal tab)]

The graphic will show a plot of the compensation response

Make sure Global box is unchecked

Click Download



3. Select Player Tab

This example will use the *Internal* clock source and *No Trigger* with repeated image play (*Repeat Forever*). Internal clock rate is set on the lower tool bar, main window

For external signals, check **External** buttons and apply:

- Trigger input to J10
- Clock input to J11

And when using with an RF amplifier with control through J5 connector of the iMS4 apply:

- Gate input to J9

Note: If you do not want to use a compensation *.LUT file (see previous section), then uncheck the highlighted boxes to disable compensation.

The RF output will be zero unless a *.LUT file is downloaded into the iMS4 <u>or</u> these boxes are unchecked.

4. Select Signal Tab

Two Power Settings control the RF output level:

- **DDS** is common to all four outputs.
- Ch1, Ch2, Ch3, Ch4 sliders set the power for each output channel independently.

Typical settings when used with RFA0110-2-15 are: DDS = 70% - 90% Ch1 = Ch2 = 40% - 60% Ch3 = Ch4 = 40% - 60%

For XY AOD applications, Ch1 = Ch2 and Ch3 = Ch4

iMS4 RF output connectors. Terminate onto a 50ohm input or load.

J1 = Ch1, J2 = Ch2, J3 = Ch3, J4 = Ch4

To enable the RF amplifier output, toggle the Amplifier Enable button (-> Green)



Signal Path	tings			- ↓ ×					
Power Set	50.0%	50.0%	50.0%	50.0%					
01.2 70	30.0 78	30.0 %	30.0 %	50.0 %					
∇									
			T						
	Ch1	Ch2	Ch3	Ch4					
DDS	Int	Int	Int	Int					
Ampl	ifier Enable	2							
RF Cł	nannels 1+	2 Enable							
RF Cł	RF Channels 3+4 Enable								
Sync Data Analog Syn	Settings c Output S	ource A							
ImageAn	alogA			~					
Analog Syn	c Output S	ource B							
ImageAn	alogB			~					
Digital Sync	Output So	ource							
ImageDig	iital			~					
Digital Synd	: Output De	elay							
		0.	00 🍨 µs						
Digital Syno	Pulse Len	gth							
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		0	01 🏯 🛛	:					
Signal	Calibrat	Playe	er Cor	npe					

5. Start Image Play

Click the *Play Button* to start Image play. (It will 'grey' out).



Typical output waveforms on oscilloscope for the files and signal levels described above. Internal Clock rate 166KHz

Trace 1 = RF output (50ohm terminated); J1, J2

Trace 2 = SDIO-0; J7 pin 33, (rtn pin 26)





View into iMS4 connector J7

SDIO outputs are opto- isolated and require a separate +5V supply to operate



7: Does my iMS4 work ?

Single Tone mode provides a basic functional and communication check with the Host PC. This will generate a constant RF signal on the output channels, J1...J4 <u>There is no SDIO sync output in this mode</u>.

Select Calibration Tab

Select the **DISABLED** button. It will change to **ENABLED** and the RF outputs will be active. Adjust the Frequency and Amplitude sliders as required.

Note: This mode will prevent Image Play. <u>Return to DISABLED</u> when finished.

At 100 % amplitude, the RF output on J1, J2, J3 or J4 will be 100-650mV peak to peak into 50 ohms, depending on the power level settings in the **Signal tab** (See Section 4 above).



